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## APPLICATION INFORMATION

Title Line One:: METHOD OF MAKING HIGH-VOLTAGE BIPOLAR/CM  
Title Line Two:: OS/DMOS DEVICES  
Total Drawing Sheets:: 43  
Formal Drawings?: No  
Application Type:: Utility  
Docket Number:: 11759-US  
Secrecy Order in Parent Appl.?: No

**Figure 1** *Diagram illustrating the experimental design. The study was conducted in two phases. In Phase 1, participants were exposed to a 10-minute video of a simulated fire. In Phase 2, participants were exposed to a 10-minute video of a simulated fire. The study was conducted in two phases. In Phase 1, participants were exposed to a 10-minute video of a simulated fire. In Phase 2, participants were exposed to a 10-minute video of a simulated fire.*

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